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(12) United States Patent Howell

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(54)	METHOD AND SYSTEM FOR
	SEQUENTIALLY PROGRAMMING
	MEMORY-CONTAINING INTEGRATED
	CIRCUITS

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(51) I	Int. Cl. ⁷		G06F	12/00
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(57) ABSTRACT

Data to be programmed into memory-containing ICs is divided into a number (X) of blocks preferably equal to the number of sockets on a common programmer unit. A pick-up head inserts an unprogrammed IC into the first socket and the IC is programmed with a first data block A. While programming is occurring the pick-up head fetches an unprogrammed IC and inserts it into a second socket, whereupon both ICs are simultaneously programmed with the second data block B. During this time an unprogrammed IC is fetched and inserted into a third socket, whereupon all socketed ICs are simultaneously programmed with a third data block C. Eventually the first IC is fully programmed and is replaced with an unprogrammed IC and the cycle continues until all ICs to be programmed have been programmed. Multiple pick-up heads and/or multi-socketed programming units can be used.

21 Claims, 4 Drawing Sheets

